

GENERAL INFORMATION

SIMPL WINDOWS NAME	Intelix FLX-88 demo.smw
CATEGORY	Video switching
VERSION	1.0.0.1
SUMMARY	Controls the Intelix FLX-88 via RS-232 or IP.
GENERAL NOTES	Controls routing of sources to destinations based on analog inputs. Provides polling routine with adjustable timing. Provides EDID management including reset and copying from outputs. Provides preset management including save, reset, and clear. Provides standby control. Provides route-through and deroute all functions. Provides front panel lock out. Provides asynchronous feedback message control. Provides per-input HDCP compliance control. Provides per-input input signal type control. Provides per-input VGA auto-adjust. Provides per-input scaling controls. Provides per-input HDMI audio embedding controls. Provides true feedback.
CRESTRON HARDWARE REQUIRED	2- or 3-SERIES PROCESSORS
COMMUNICATION SETUP	RS232: 9600 Baud, No Parity, 8 Data Bits, 1 Stop Bit TCP/IP: Port 4001
VENDOR FIRMWARE	Pro2: 4.007.0004 MC3: 1.005.0007 FLX-88: 1.5.4

CONTROL

rx	S	Connect to serial or IP receive input
Output_[1-8]	A	Set to input number to route to corresponding output
Reset_Matrix_EDID	D	Set all inputs to factory EDID settings
Input_[1-8]_EDID_From_Output	A	Set output from which to copy EDID for specified input
Recall_Preset_[0-9]	D	Pulse to recall matrix preset
Save_Preset_[0-9]	D	Pulse to save matrix preset
Clear_Preset_[0-9]	D	Pulse to clear/erase stored matrix preset
Power_On	D	Activate matrix power

Power_Off	D	Put matrix into standby mode
Route_All_Inputs_Through	D	Pulse to route input 1 to output 1, 2 to 2, etc.
Unroute_All_Outputs	D	Pulse to unroute all outputs
Front_Panel_Lock	D	Pulse to lock front panel
Front_Panel_Unlock	D	Pulse to unlock front panel
Feedback_Messages_On	D	Pulse to enable front panel routing feedback
Feedback_Messages_Off	D	Pulse to disable front panel routing feedback
Poll_Enable	D	Enable true feedback polling at the rate specified by “poll rate”
Input_[1-8]_HDCP_Compliance_On	D	Pulse to enable HDCP compliance on specified input
Input_[1-8]_HDCP_Compliance_Off	D	Pulse to disable HDCP compliance on specified input
Input_[1-8]_FLX-RI4_Signal_Type_VGA	D	Pulse to set FLX-PI4 input for VGA signal
Input_[1-8]_FLX-RI4_Signal_Type_Component	D	Pulse to set FLX-PI4 input for Component signal
Input_[1-8]_FLX-RI4_Signal_Type_S-Video	D	Pulse to set FLX-PI4 input for S-Video signal
Input_[1-8]_FLX-RI4_Signal_Type_Composite	D	Pulse to set FLX-PI4 input for Composite signal
Input_[1-8]_FLX-RI4_VGA_Auto-Adjust	D	Pulse to auto-adjust FLX-RI4 input in VGA mode
Input_[1-8]_FLX-RI4_Scaler_Resolution_1024x768_(XGA)	D	Pulse to set FLX-RI4 input to be scaled to XGA resolution
Input_[1-8]_FLX-RI4_Scaler_Resolution_1280x720_(720p)	D	Pulse to set FLX-RI4 input to be scaled to 720p resolution
Input_[1-8]_FLX-RI4_Scaler_Resolution_1280x800_(WXGA)	D	Pulse to set FLX-RI4 input to be scaled to WXGA resolution
Input_[1-8]_FLX-RI4_Scaler_Resolution_1920x1080_(1080p)	D	Pulse to set FLX-RI4 input to be scaled to 1080p resolution
Input_[1-8]_FLX-HI4A_Audio_Source_Digital_(HDMI)	D	Pulse to capture digital audio for specified FLX-HI4A input

Input_[1-8]_FLX-HI4A_Audio _Source_Analog -(Euroblock)	D	Pulse to capture analog audio for specified FLX-HI4A input
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PARAMETERS

Poll Rate	P	How long to wait between polling cycles
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FEEDBACK

tx	S	Connect to serial or IP transmit output
Output_[1-8]_fb	A	True feedback, input currently routed to specified output
Power_On_fb	D	True feedback, high when unit reports on
Power_Off_fb	D	True feedback, high when unit reports standby
Front_Panel_Lock_fb	D	True feedback, high when unit reports front panel locked
Front_Panel_Unlock_fb	D	True feedback, high when unit reports front panel unlocked
Feedback_Messages_On_fb	D	True feedback, high when unit reports front panel routing feedback enabled
Feedback_Messages_Off_fb	D	True feedback, high when unit reports front panel routing feedback disabled